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Jeanette E. Boose

(Depositor's name)

Jeanette E. Boose

(Signature)

November 14, 2006

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY/DOCKET NO.	CONFIRMATION NO.
10/812,378	03/29/2004	Yi-Hsun Wu	N1280-00120(TSMC2003-0919	2646

TITLE OF INVENTION: OUTPUT BUFFER ESD PROTECTION USING PARASITIC SCR PROTECTION CIRCUIT FOR CMOS VLSI INTEGRATED CIRCUITS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$0	\$1700	11/22/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
BAUER, SCOTT ALLEN	2836	361-091100

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Duane Morris, LLP

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Taiwan Semiconductor Manufacturing Co., Ltd

HSIN-CHU, TAIWAN

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☐ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Mark J. Marcelli

Date November 14, 2006

Typed or printed name

Mark J. Marcelli

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